

FIG. 1

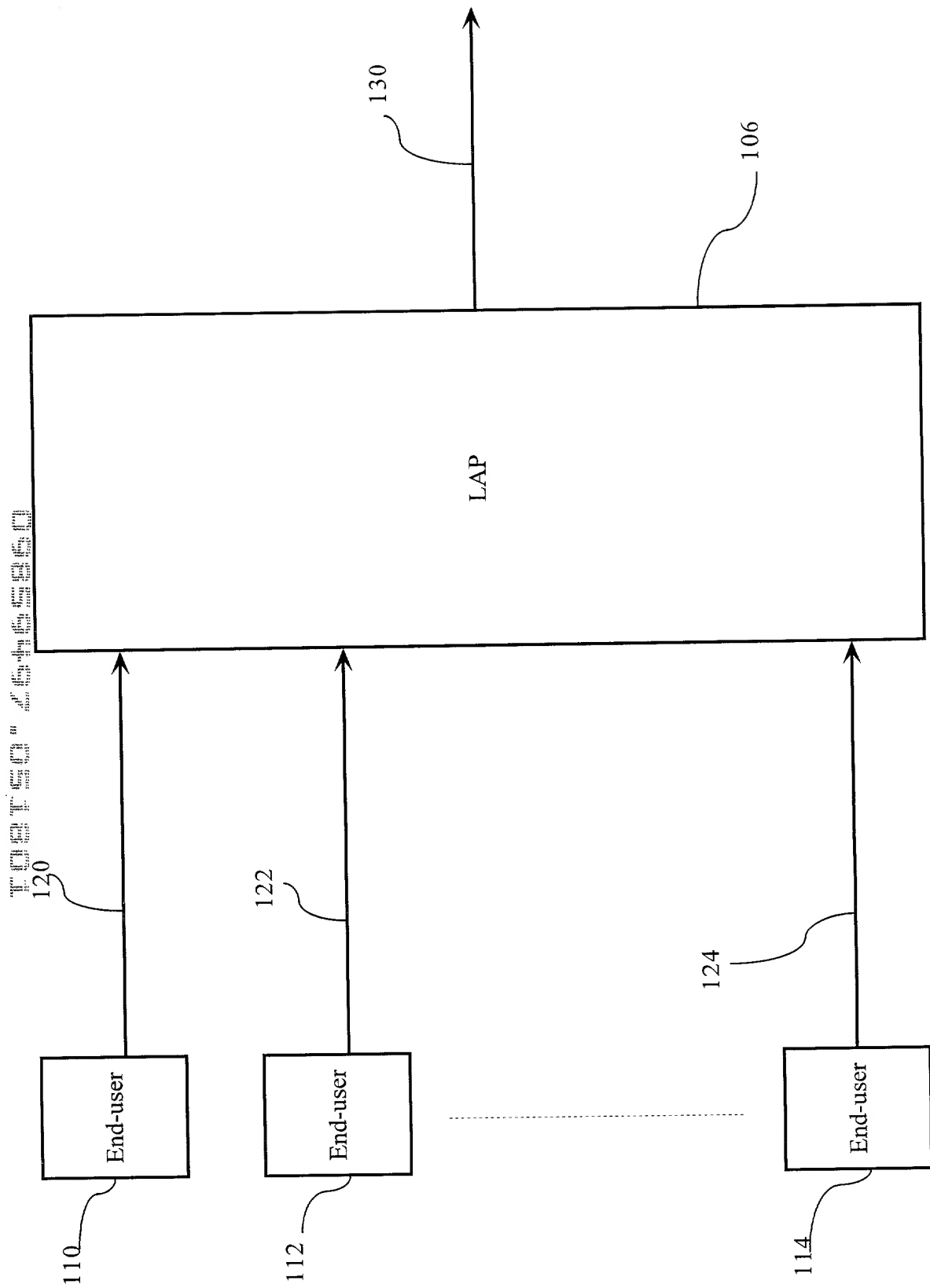


FIG. 2

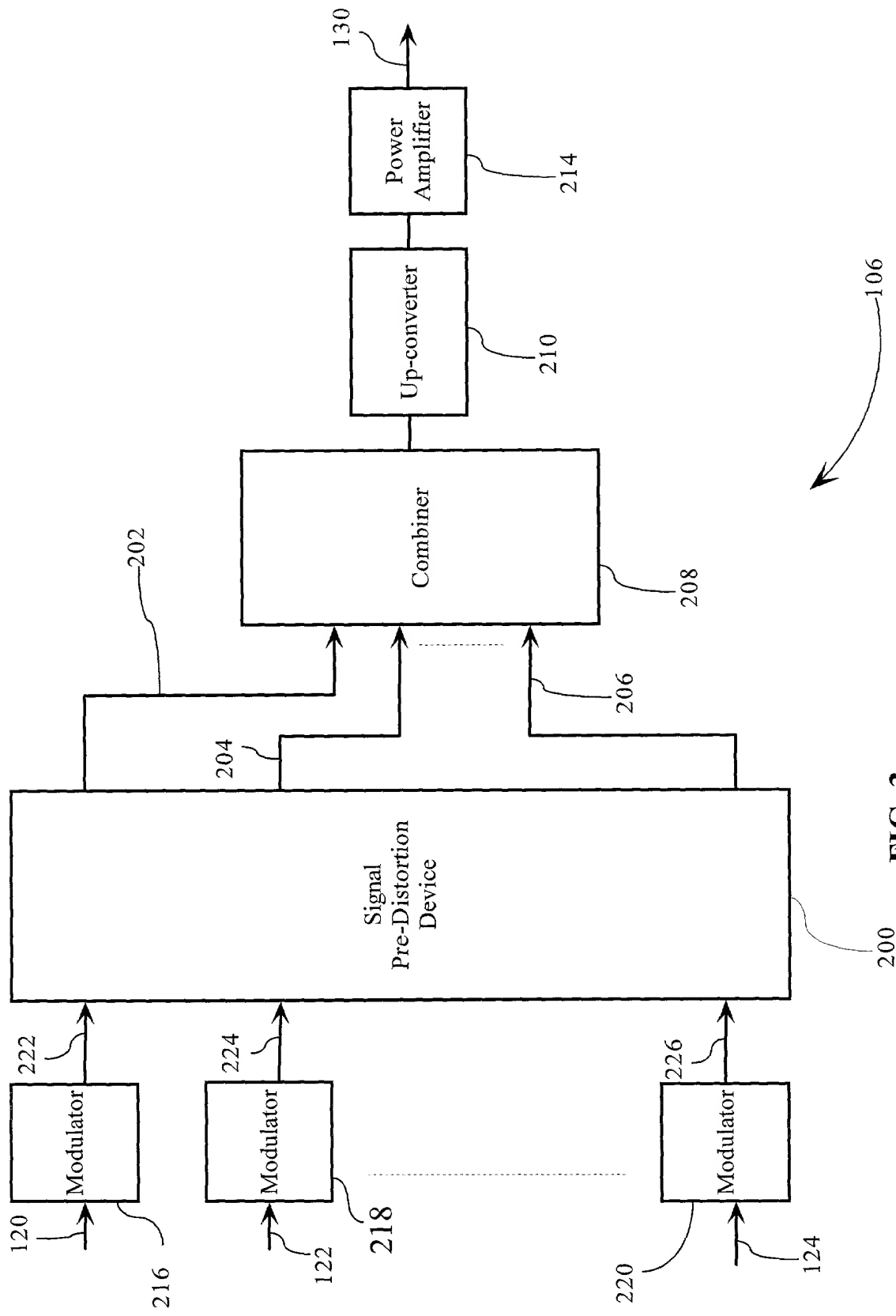


FIG. 3

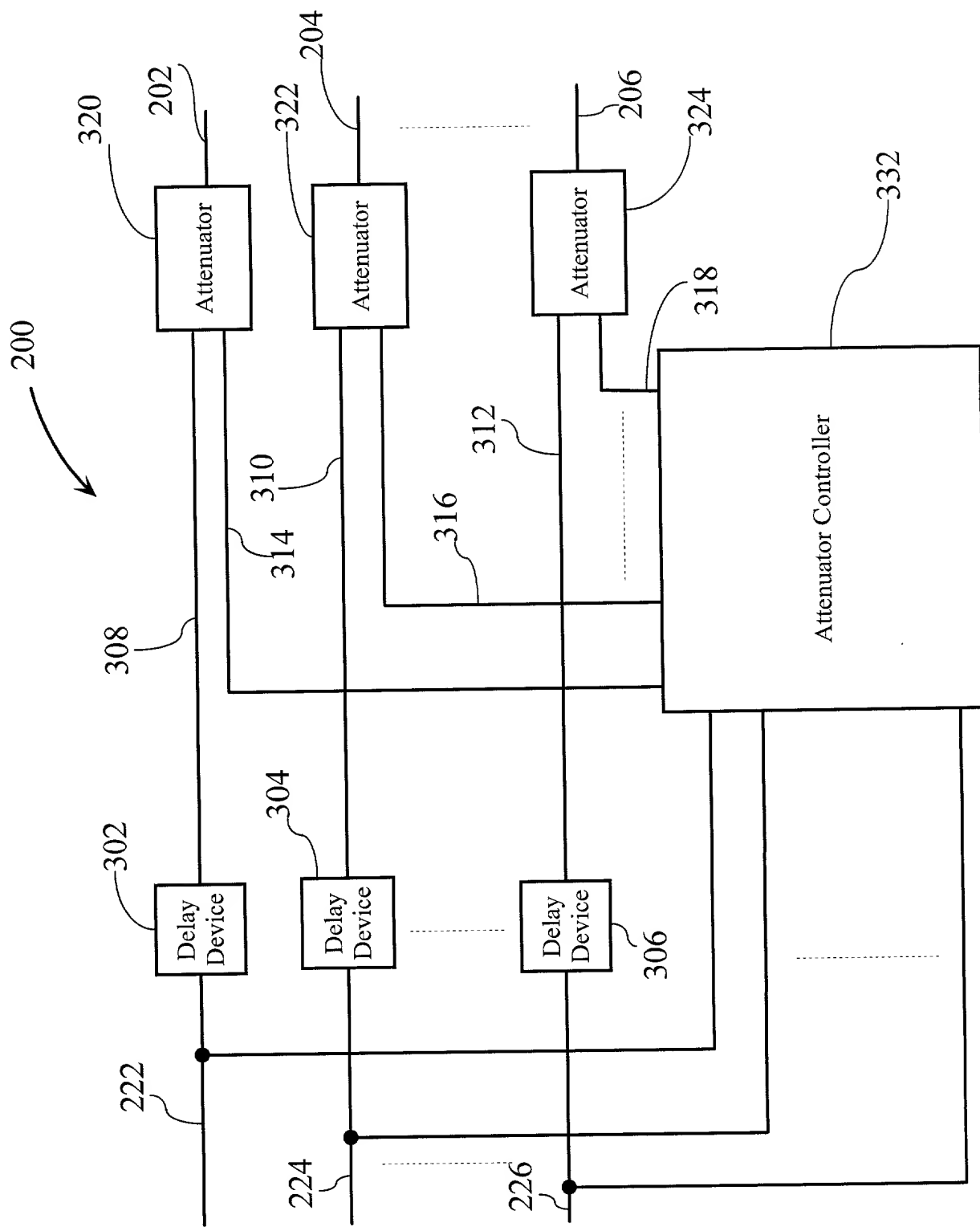


FIG. 4

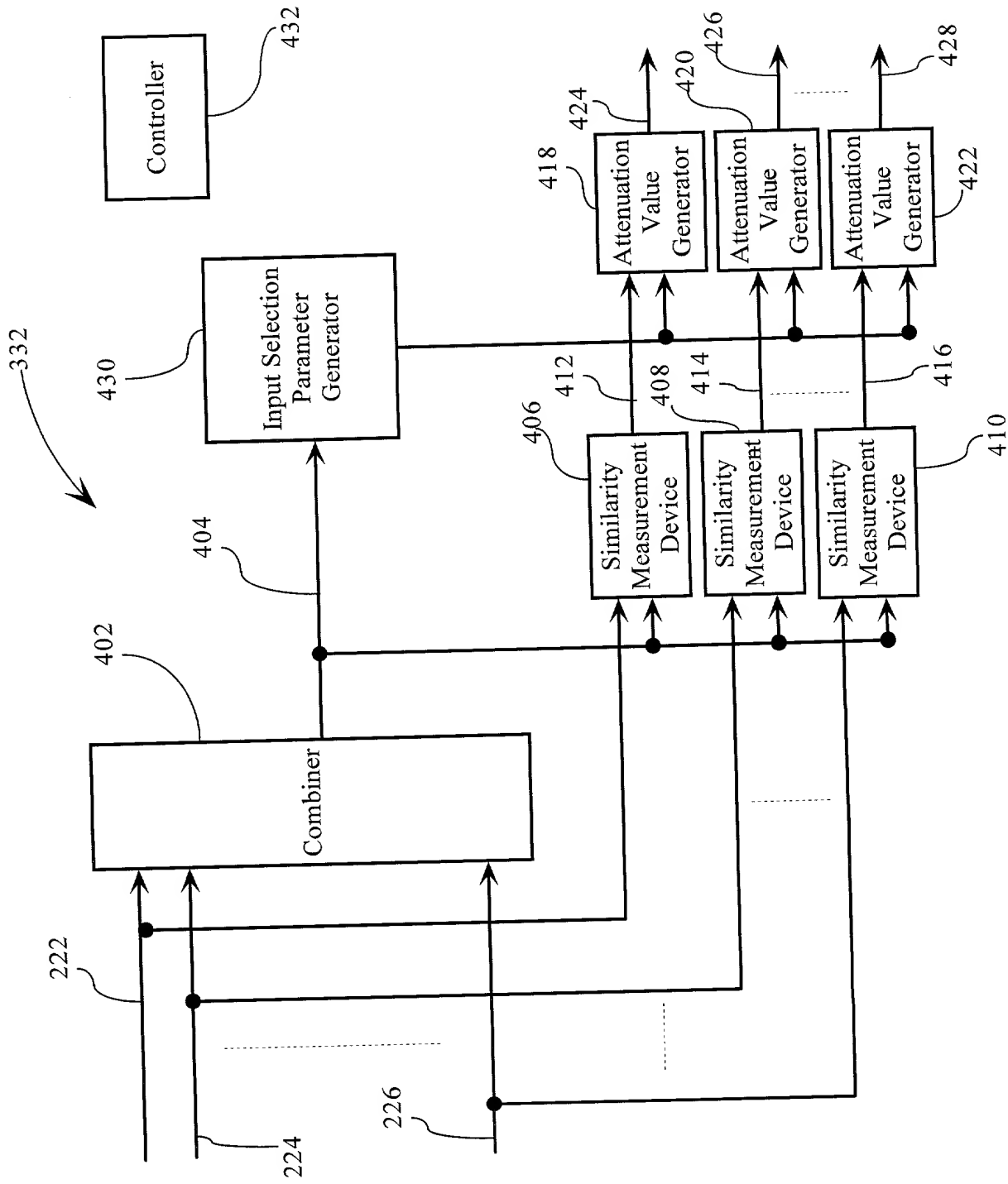


FIG. 5

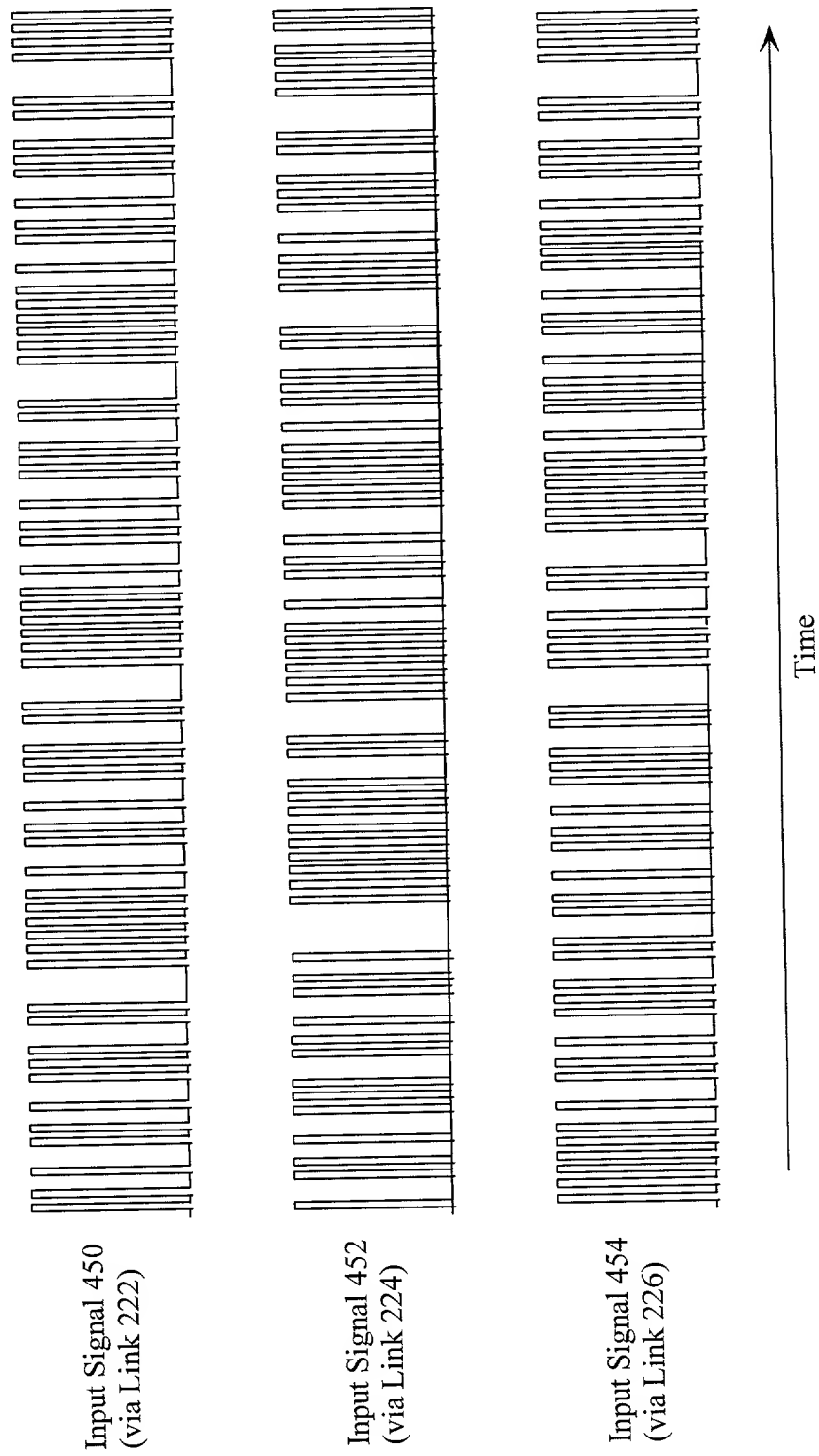


Fig. 6

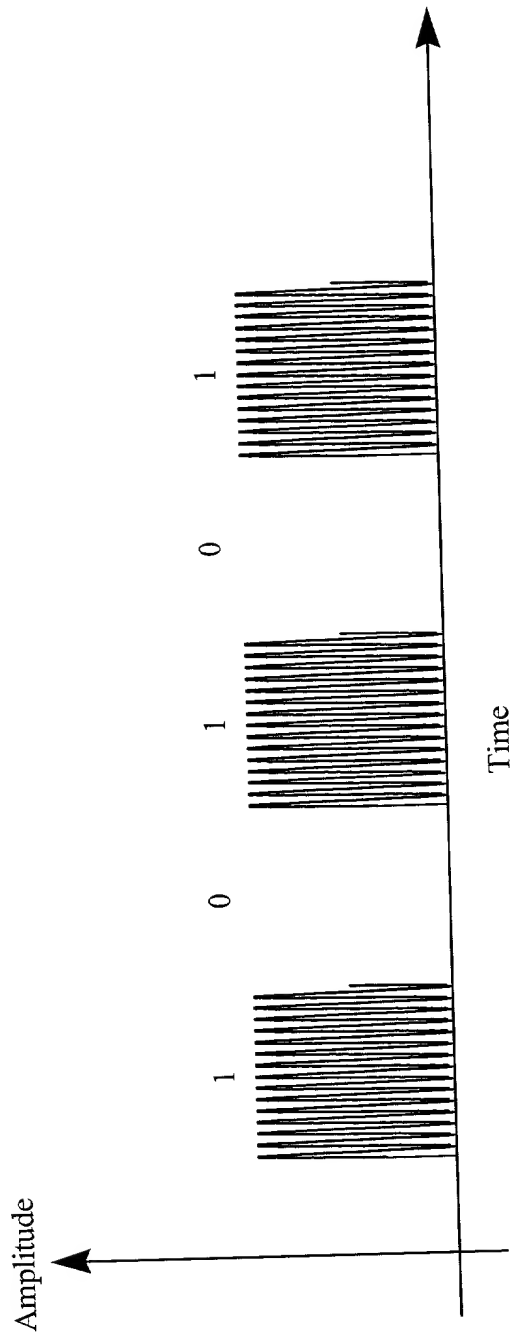


Fig. 7

FIG. 8 is a block diagram of a system for processing a signal.

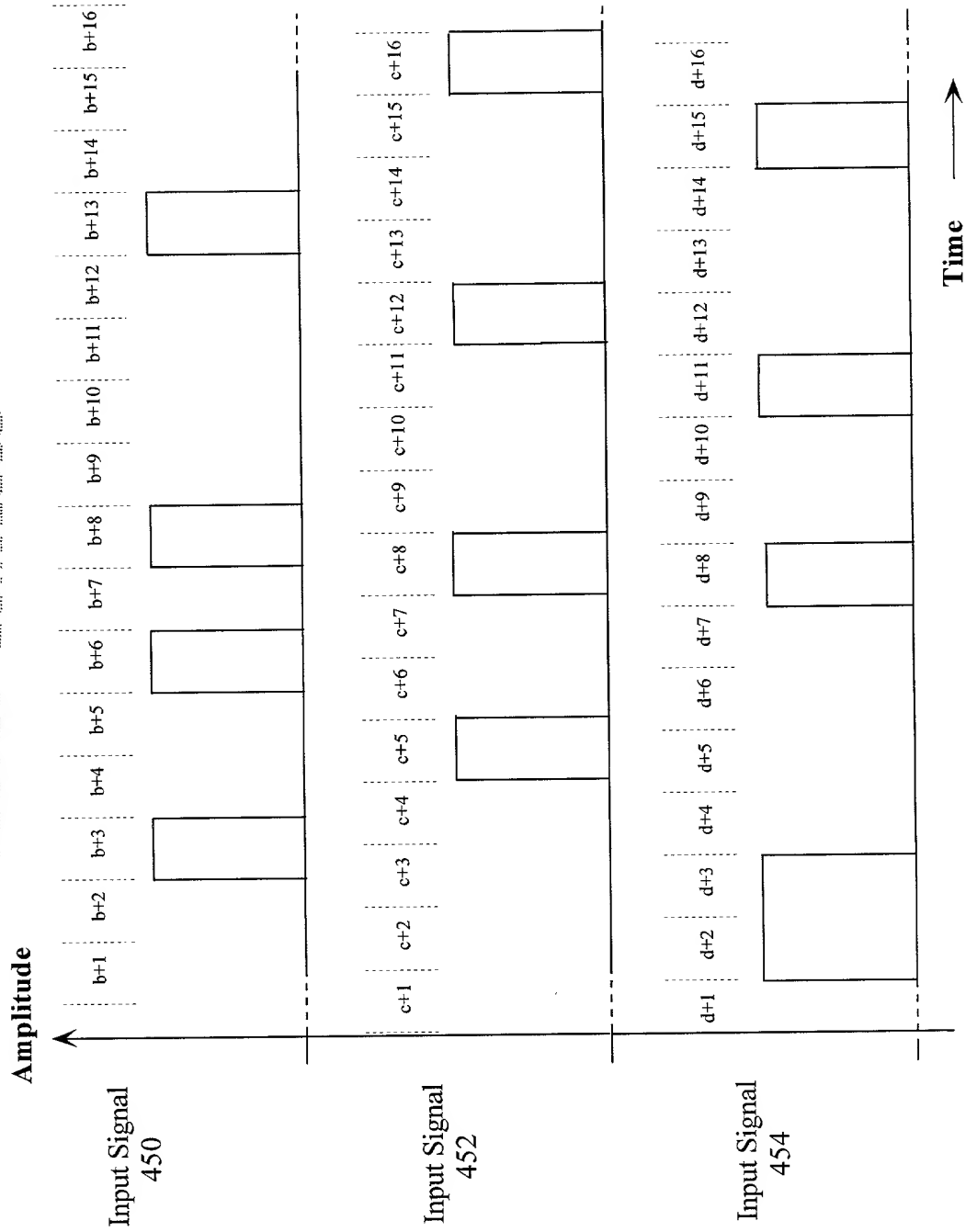


FIG. 8



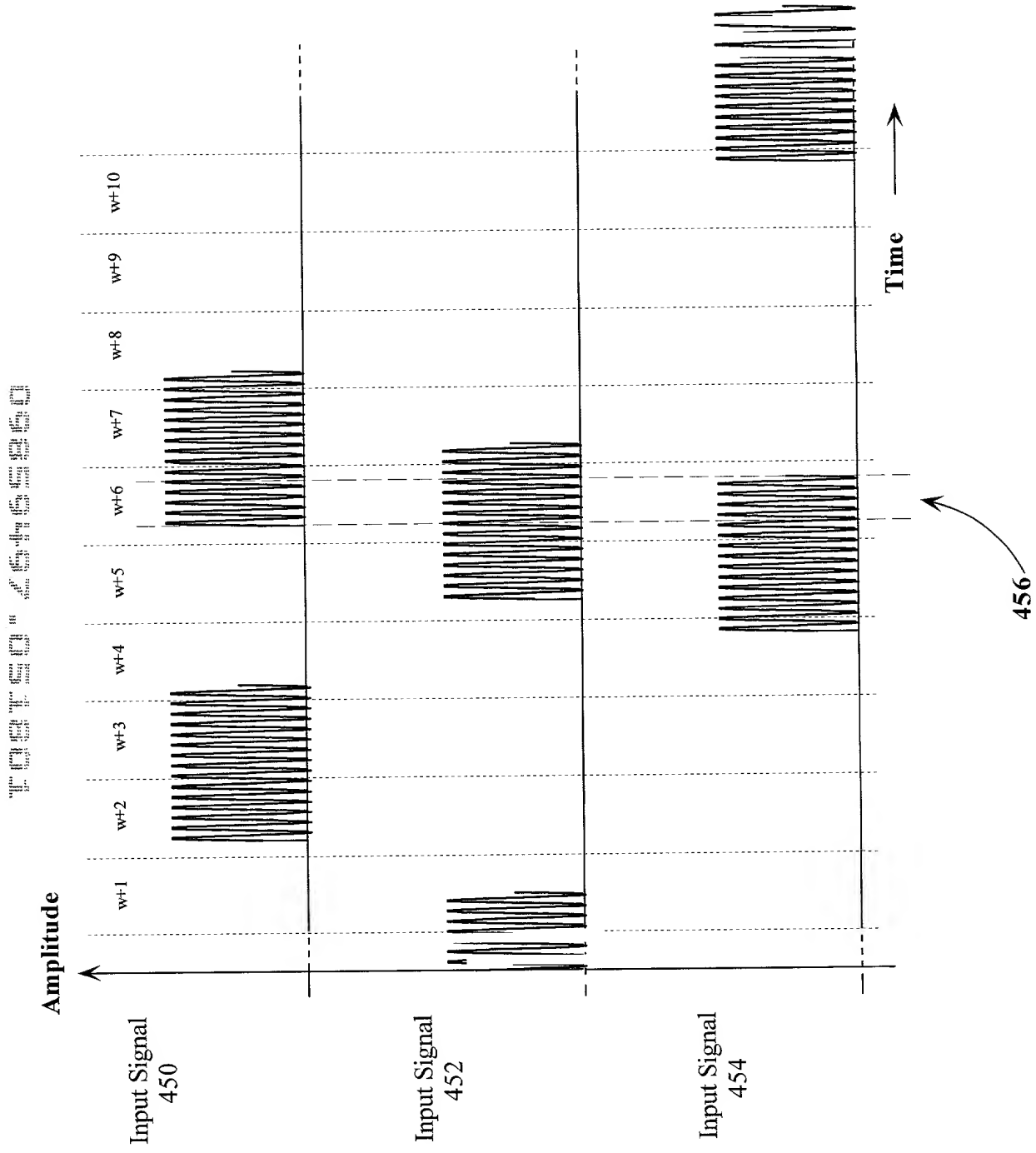


FIG. 9

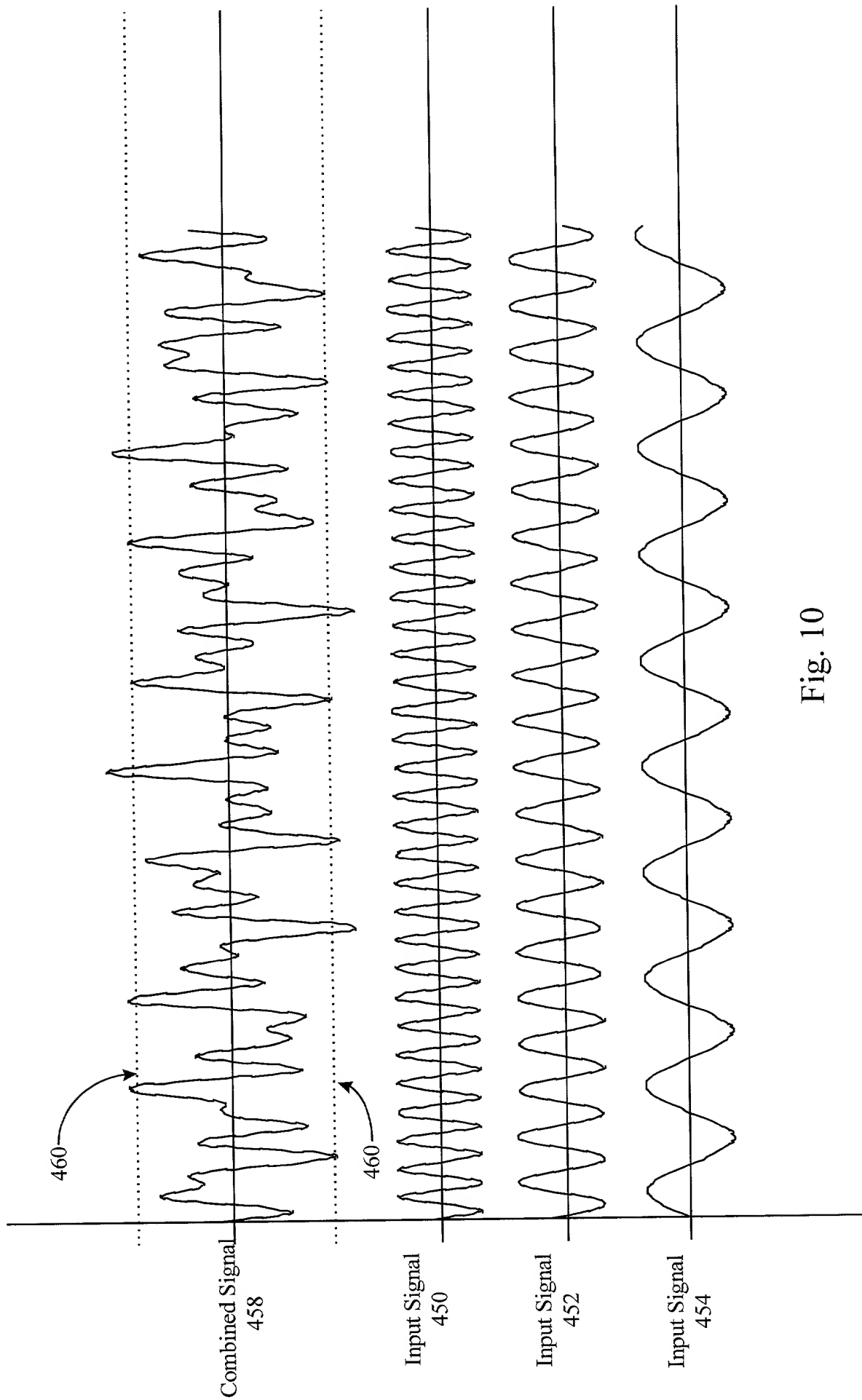


Fig. 10

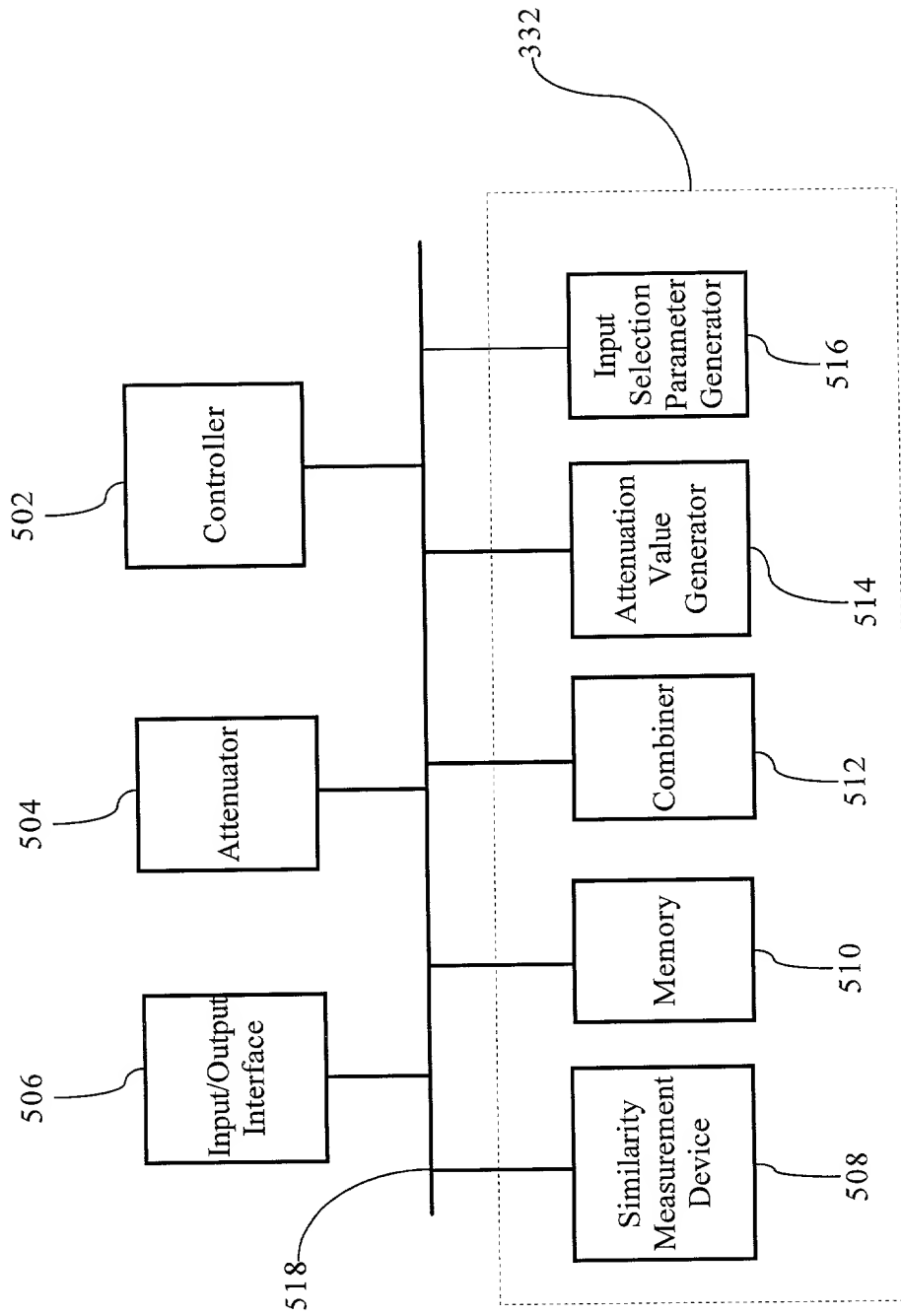


FIG. 11

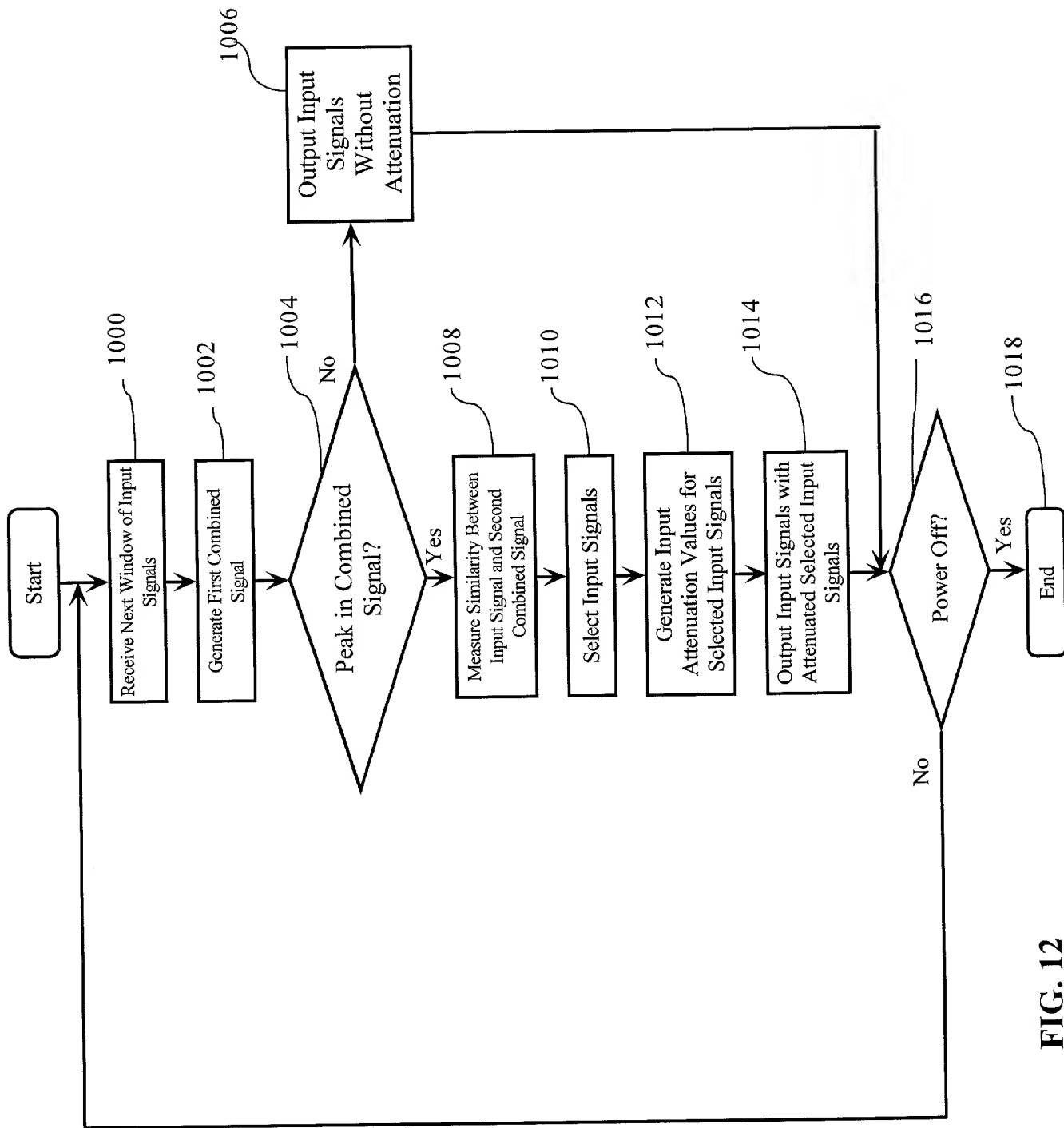


FIG. 12